

Figure 1

Only Bus Trip Operator



100

Operational Flowchart

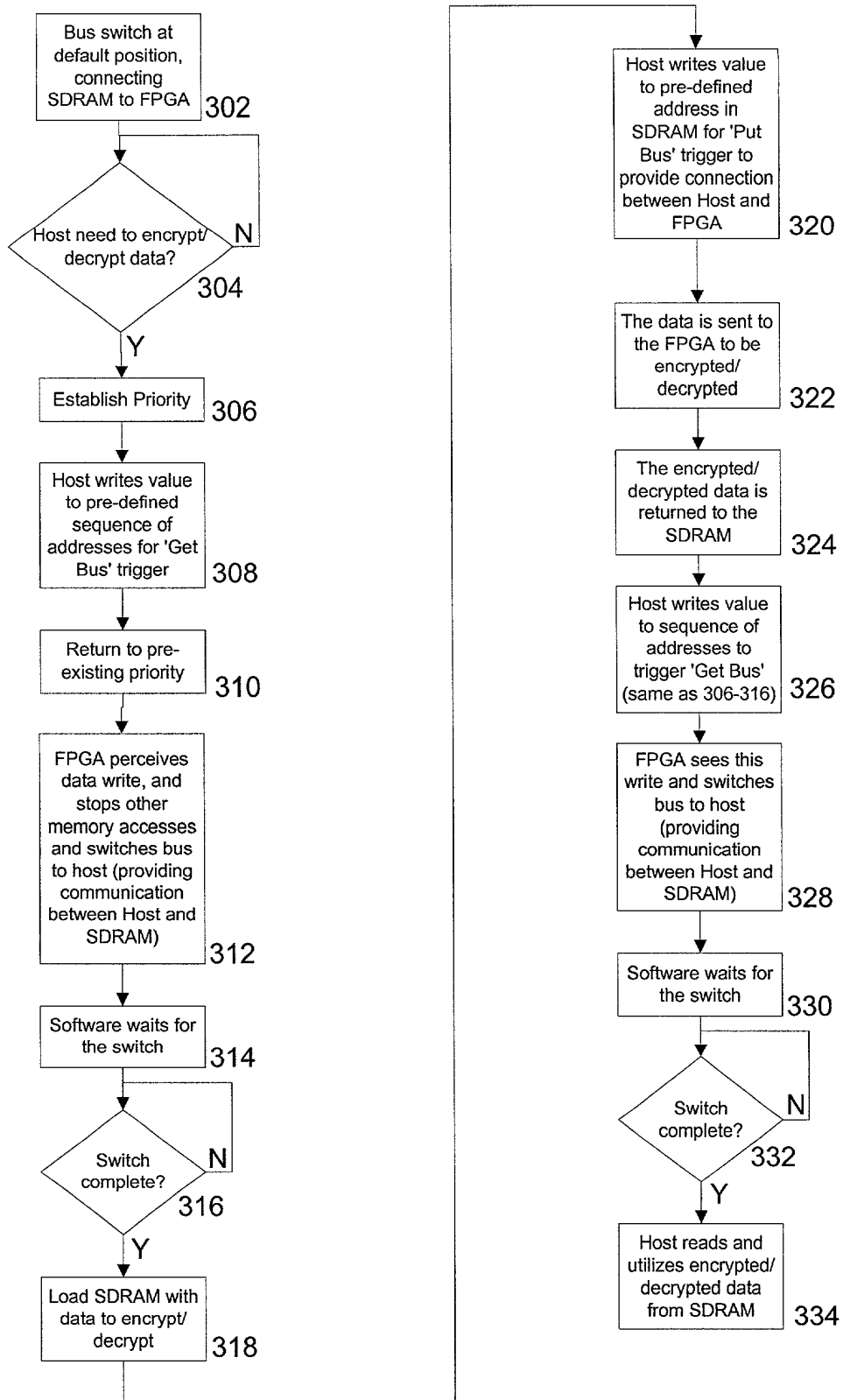


Figure 3

Example Memory Module Trigger Address Locations

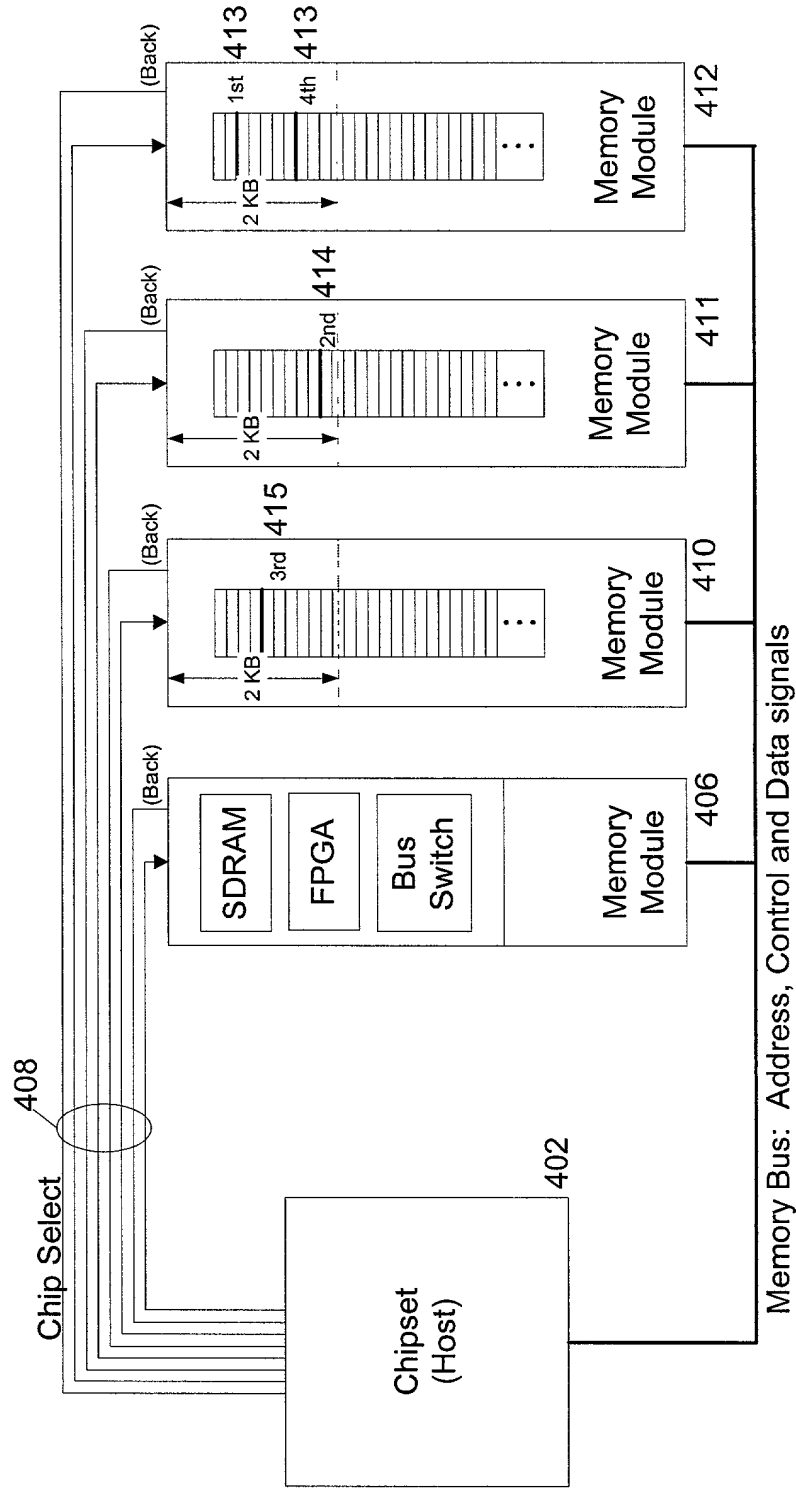


Figure 4